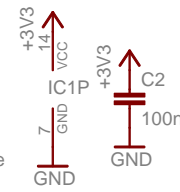
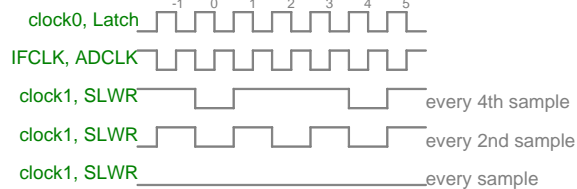
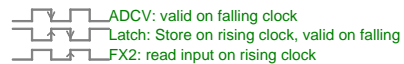


Difference to Rev.1:  
We can't use PKTEND as IO pin!



10MS/s:  
sync fifo mode; SLWR=LOW  
IFCLK=extern

5MS/s and less:  
async fifo mode; SLWR=clock  
IFCLK=intern (internal clock, oe=0)

USB Live Oscilloscope	
Wolfgang Wieser 01/2006, 06/2006	
TITLE: MainBoard-r1a	
Document Number:	REV: 1a
Date: 6/18/2006 15:02:44	Sheet: 1/1